

DOCKET NO.: Intel 2207/10083 ASSIGNEE: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant No.

09/751,750

Confirmation No. 5033

Applicant

Talal K. JABER

Filed

December 29, 2000

RECEIVED

JUL 1 9 2004

For

M

MICROPROCESSOR ON-CHIP TESTING

ARCHITECTURE AND IMPLEMENTATION Technology Center 2100

Group Art Unit

2133

Examiner

Guy J. LAMARRE

M/S:

COMMISIONER FOR PATENTS P.O. Box 1450

Alexandria, VA 22313-1450

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: M/S; Commissioner for Patents, P.O. Box 1450, Alexandria, VA

22313-1450 on

Date: July 7, 2004 Signature:

Blanche Guzman-Salmon

AMENDMENT

Sir:

In response to the Office Action issued January 12, 2004, please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.